

## **DATA SHEET**

# Arista Aggregators - 7020SR & 7280R Range

# 24 to 64 PORTS Aggregators

- 100 Mbps to 100 Gbps
- ✓ Up to 64 ports
- ✓ "n" aggregated ports to analysis ports
- ✓ CLI commands

- Very low latency
  - Open Platform
  - Easy to use
  - Reduced costs

Controlling the flows exchanged on the information system requires the implementation of multiple analysis solutions, whether for massive frame capture, analyzing the performance of networks and applications or implementing systems of security.

The cost of these solutions makes it necessary to optimize flow acquisition and interception architectures by integrating aggregation matrices. Must therefore:

- implement port mirroring functions or deploy TAPs on the links to be analyzed,
- direct the copied flows to the input ports of the matrix,
- aggregate the matrix input copy ports to "n" output ports (ports tools),
- connect the aggregated ports of the copied streams to the acquisition ports of the analysis solutions.

This approach reduces the number of solutions deployed and thus avoids the explosion of budgets devoted to analysis and security tools.

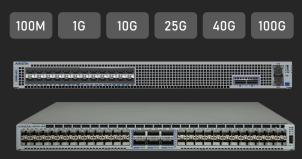
### **Arista Range**

The Arista range of aggregation matrices (commonly referred to as NPB for Network Packet Brokers) meets the main market expectations in the field. Their architecture is open and technical team personnel can easily get to grips with them through a dedicated graphical HMI or even by using native CLI commands which they are mostly already trained in.

The 7020SR and 7280R ranges cover all needs in this area. With backplanes at considerable speeds that have proven themselves in high-frequency trading, they guarantee to feed the analysis tools connected to them without loss of packets. In addition, their costs are extremely competitive.

It is these technical criteria and price, combined with high-level support, that prompted Allentis to choose Arista to complete its TAP offer in the implementation of traffic acquisition and interception architectures.

By combining Arista matrices with TAP Tapics deployed on major IS for flow analysis, allentis delivers "turnkey" solutions for complete acquisition architectures to which probes are connected.



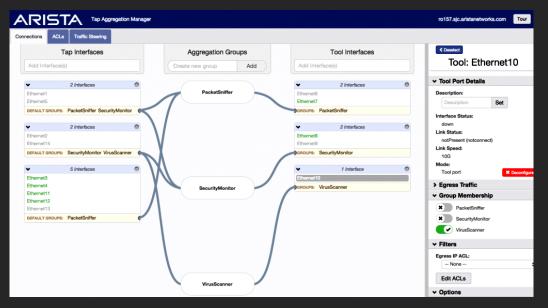
Arista 7020SR 24 Ports and 7280R 48 Ports

Model	7020SR-24	7020SR-32
Ports	24 SFP+ & 2QSFP	32SFP+ & 2QSFP
Bandwidth.	100 Gbps	100 Gbps
Max. bandwidth	480 Gbps	1,04Tbps
Latency	À partir de 3,8μs	À partir de 3,8μs
СРИ	Dual Core x86	Dual Core x86
Mem Packet Buffer	3 Go	3 Go
electric Power	95/105 W	160/189 W
Dim. (cm)	44 x 4,4 x 40	44 x 4,4 x 34
Weight (kg)	6,4	7,5
Réf.	TAAG-7020SR-24C2	TAAG-7020SR-32C2

Model	7280SR3-40YC6	7280SR3-48YC8
Ports	40SFP+ & 6QSFP	48SFP+ & 8QSFP
Bandwidth	100 Gbps	100 Gbps
Max. bandwidth	2 Tbps	2 Tbps
Latency	À partir de 3,8µs	À partir de 3,8µs
СРИ	QuadCorex86	QuadCore x86
Mem Packet Buffer	4Go	4Go
Puiss. Typique/Max	154/324 W	154/324 W
Dim. (cm)	44 x 4,4 x 47	44 x 4,4 x 47
Weight (kg)	9,18	9,18
Réf.	TAAG-7280SR3-40YC6	TAAG-7280SR3-48YC8

### **Extended Configuration Capabilities**

Beyond the possibilities of aggregating all the traffic on the input ports, the configuration and filtering capabilities available on the Arista matrices allow the most advanced configurations expected.



Defining aggregation rules to multiple analysis ports

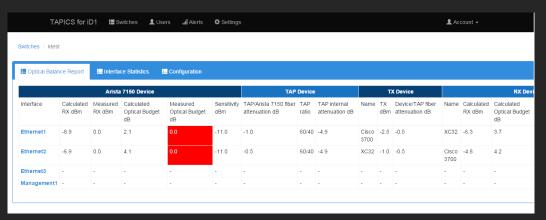
The user can thus rely on the following main technical characteristics in order to define the rules adapted to the operating needs.

- Level 2 filtering (VLAN, MAC Address, MPLS etc...)
- Level 3 and 4 filtering (IP Sce/Dest, GTP, UDP/TCP Ports, dscp, etc...)
- Filtering by Pattern
- time stamping (to be indicated when ordering)
- traffic steering, ACL
- frame truncation or not
- VLAN Stripping
- LANZ+/ DANZ features with Z license

### Real-time optical balance with Allentis TSDA software

The opening of Arista platforms makes it possible to embed software specific to each user. TSDA developed by allentis calculates in real time the optical balance on the IS connected to the flow replication solution put in place.

It alerts operators in the event of power loss according to the parameters defined by the administrators and guarantees the continuous optimal operability of the entire system architecture.



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